

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	72	first adj register with first adj clock same second adj register with second adj clock	USPAT	OR	OFF	2006/07/21 13:04
S2	0	first adj register with first adj clock same second adj register with second adj clock and standby with normal	USPAT	OR	OFF	2006/07/21 12:34
S3	3	first adj register with first adj clock same second adj register with second adj clock and standby	USPAT	OR	OFF	2006/07/21 12:34
S4	29	register near5 (type adj event)	USPAT	OR	OFF	2006/07/21 13:43
S6	252	clock near3 stop\$4 near3 register	USPAT	OR	OFF	2006/07/21 13:49
S7	3	clock near3 stop\$4 near3 register with event	USPAT	OR	OFF	2006/07/21 13:50
S8	7	power with (first adj circuit) with (second adj circuit) with (standby sleep)	USPAT	OR	OFF	2006/07/21 14:02
S9	8	power with (first adj circuit) with (second adj circuit) with (standby sleep)	US-PGPUB	OR	OFF	2006/07/21 14:05
S10	3	power with (first adj circuit) with (second adj circuit) with (standby sleep)	EPO; JPO; IBM_TDB	OR	OFF	2006/07/21 14:24
S11	2	clock near5 (register near2 hold\$3) near5 stop\$4	USPAT	OR	OFF	2006/07/21 14:44
S12	1	clock near5 (register near2 hold\$3) near5 stop\$4	US-PGPUB	OR	OFF	2006/07/21 14:49
S13	2	clock near5 (register near2 hold\$3) near5 stop\$4	EPO; JPO; IBM_TDB	OR	OFF	2006/07/21 14:49
S14	278	supply\$3 adj power near7 standby	USPAT	OR	OFF	2006/07/21 15:14
S15	79	supply\$3 adj power near7 standby with circuit	USPAT	OR	OFF	2006/07/21 15:14
S16	6	supply\$3 adj power near7 standby with circuit with only	USPAT	OR	OFF	2006/07/21 15:19
S17	52	supply\$3 adj power near7 standby with circuit and power adj (conserv\$5 consum\$5 sav\$3 reduc\$5)	USPAT	OR	OFF	2006/07/21 15:19
S18	104	power adj (sav\$3 conserv\$5 consum\$5 reduc\$5) and stop\$3 near3 clock near3 register	USPAT	OR	OFF	2006/07/21 15:39
S19	0	power adj (sav\$3 conserv\$5 consum\$5 reduc\$5) and stop\$3 near3 clock near3 register same copy\$3 with register	USPAT	OR	OFF	2006/07/21 15:37

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S20	4	power adj (sav\$3 conserv\$5 consum\$5 reduc\$5) and stop\$3 near3 clock near3 register and copy\$3 with register	USPAT	OR	OFF	2006/07/21 15:37
S21	12	standby same shadow adj register	USPAT	OR	OFF	2006/07/24 17:15
S22	1	standby same shadow adj register	US-PGPUB	OR	OFF	2006/07/24 17:19
S23	1	standby same shadow adj register	EPO; JPO; IBM_TDB	OR	OFF	2006/07/24 17:19
S24	1648	standby with clock	USPAT	OR	OFF	2006/07/24 17:26
S25	853	standby adj (mode state) with clock	USPAT	OR	OFF	2006/07/24 17:26
S26	113	standby adj (mode state) with clock same register	USPAT	OR	OFF	2006/07/24 17:26
S27	77	standby adj (mode state) with clock same register with clock	USPAT	OR	OFF	2006/07/24 17:27
S28	1926	standby adj (mode state) with power adj (consum\$5 conserv\$5 sav\$3 reduc\$5)	USPAT	OR	OFF	2006/07/24 17:39
S29	14	standby adj (mode state) with power adj (consum\$5 conserv\$5 sav\$3 reduc\$5) with register	USPAT	OR	OFF	2006/07/24 17:39
S30	12	standby adj (mode state) with power adj (consum\$5 conserv\$5 sav\$3 reduc\$5) with register	US-PGPUB	OR	OFF	2006/07/24 17:48
S31	6	standby adj (mode state) with power adj (consum\$5 conserv\$5 sav\$3 reduc\$5) with register	EPO; JPO; IBM_TDB	OR	OFF	2006/07/24 17:50
S32	209	clock near2 stop\$4 near3 register	USPAT	OR	OFF	2006/07/25 15:59
S33	9	clock near2 stop\$4 near3 register same power adj (sav\$3 conserv\$5 consum\$5 reduc\$5)	USPAT	OR	OFF	2006/07/25 16:00
S34	9	clock near2 stop\$4 near3 register same power adj (sav\$3 conserv\$5 consum\$5 reduc\$5)	US-PGPUB	OR	OFF	2006/07/25 16:33
S35	5	clock near2 stop\$4 near3 register same power adj (sav\$3 conserv\$5 consum\$5 reduc\$5)	EPO; JPO; IBM_TDB	OR	OFF	2006/07/25 16:37
S36	4	register near5 event with stop\$4 near2 clock	USPAT	OR	OFF	2006/07/25 17:25
S37	2	register near5 event with stop\$4 near2 clock	US-PGPUB	OR	OFF	2006/07/25 17:32
S38	0	register near5 event with stop\$4 near2 clock	EPO; JPO; IBM_TDB	OR	OFF	2006/07/25 17:32
S39	112	shadow adj register and power adj (sav\$3 conserv\$5 consum\$5 reduc\$5)	USPAT	OR	OFF	2006/07/25 18:19
S40	63	shadow adj register with clock	USPAT	OR	OFF	2006/07/25 18:21

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S41	0	shadow adj register with clock same standby	USPAT	OR	OFF	2006/07/25 18:20
S42	1	shadow adj register with clock and standby	USPAT	OR	OFF	2006/07/25 18:20
S43	0	shadow adj register with wake\$1up	USPAT	OR	OFF	2006/07/25 18:21
S44	0	shadow adj register same wake\$1up	USPAT	OR	OFF	2006/07/25 18:22
S45	3	shadow adj register same bus adj register	USPAT	OR	OFF	2006/07/25 18:22
S46	2	shadow adj register same bus adj register	US-PGPUB; EPO; JPO; IBM_TDB	OR	OFF	2006/07/25 18:28
S47	2	("6664148" "5842028").pn.	USPAT	OR	OFF	2006/07/27 09:47
S48	1	("6664148").URPN.	USPAT	OR	OFF	2006/07/27 09:54
S49	6	("6046627" "6049245" "6100751" "6225852" "6300197" "6518104").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/07/27 09:54
S50	1262	circuit near5 (first adj clock) same circuit near5 (second adj clock)	USPAT	OR	OFF	2006/07/27 10:00
S51	75	(first adj circuit) near5 (first adj clock) same circuit near5 (second adj clock)	USPAT	OR	OFF	2006/07/27 10:00
S52	31	(first adj circuit) near5 (first adj clock) same (first adj circuit) near5 (second adj clock)	USPAT	OR	OFF	2006/07/27 10:07
S53	8	(first adj circuit) near5 (first adj clock) same (first adj circuit) near5 (second adj clock) and power adj (sav\$3 consum\$5 conserv\$5 reduc\$5)	USPAT	OR	OFF	2006/07/27 10:07
S54	0	("6879196").URPN.	USPAT	OR	OFF	2006/07/27 10:19
S55	6	("4496861" "4922141" "5118975" "5900752" "6072347" "6184733").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/07/27 10:19
S56	10	(first adj circuit) near5 (first adj clock) same (first adj circuit) near5 (second adj clock) and power adj (sav\$3 consum\$5 conserv\$5 reduc\$5)	US-PGPUB	OR	OFF	2006/07/27 10:20
S57	0	(first adj circuit) near5 (first adj clock) same (first adj circuit) near5 (second adj clock) and power adj (sav\$3 consum\$5 conserv\$5 reduc\$5)	EPO; JPO; IBM_TDB	OR	OFF	2006/07/27 10:23
S58	6	power near3 (only adj circuit) near3 (standby mode)	USPAT	OR	OFF	2006/07/27 10:25
S59	4	power near3 (only adj circuit) near3 (standby mode)	US-PGPUB	OR	OFF	2006/07/27 11:10
S60	0	power near3 (only adj circuit) near3 (standby mode)	EPO; JPO; IBM_TDB	OR	OFF	2006/07/27 11:12

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S61	311	circuit with (first adj (memory register)) with (second adj (memory register)) with clock\$1	USPAT	OR	OFF	2006/07/27 11:18
S62	61	circuit with (first adj (memory register)) with (second adj (memory register)) with clock\$1 and power adj (sav\$3 conserv\$5 consum\$5 reduc\$5)	USPAT	OR	OFF	2006/07/27 11:20
S63	3	circuit with (first adj (memory register)) with (second adj (memory register)) with clock\$1 and power adj (sav\$3 conserv\$5 consum\$5 reduc\$5) and copy\$3 near5 (register memory)	USPAT	OR	OFF	2006/07/27 11:20
S64	56	standby adj control\$4 adj circuit	USPAT	OR	OFF	2006/07/27 11:23
S65	6	standby adj control\$4 adj circuit with (memor\$3 register\$1)	USPAT	OR	OFF	2006/07/27 11:23
S66	2	standby adj control\$4 adj circuit with (memor\$3 register\$1)	EPO; JPO; IBM_TDB	OR	OFF	2006/07/27 11:27
S67	7	control adj (circuit unit block) with (memory register) with shadow adj (memory register) same copy\$3 with (memory register)	USPAT	OR	OFF	2006/07/27 11:33
S68	0	control adj (circuit unit block) with (memory register) with shadow adj (memory register) same copy\$3 with (memory register)	EPO; JPO; IBM_TDB	OR	OFF	2006/07/27 11:38
S69	413	(circuit unit) with ((register memory) near3 clock) with (second another fast\$2 slow\$2) adj clock	USPAT	OR	OFF	2006/07/27 14:27
S70	92	(circuit unit) with ((register memory) near3 clock) with (second another fast\$2 slow\$2) adj clock and power adj (sav\$3 reduc\$5 consum\$5 conserv\$5)	USPAT	OR	OFF	2006/07/27 14:28
S71	20	(circuit unit) with ((register memory) near3 clock) with (second another fast\$2 slow\$2) adj clock and power adj (sav\$3 reduc\$5 consum\$5 conserv\$5) and g06f\$.ipc.	USPAT	OR	OFF	2006/07/27 14:28